

Errata: CS4412A Rev. A1 Silicon

(Reference CS4412A_DS786A1 Data Sheet)

- ◆ The internal ramp feature must never be enabled. The RAMP pin must always be tied low.

Contacting Cirrus Logic Support

For all product questions and inquiries, contact a Cirrus Logic Sales Representative.
To find the one nearest you, go to <http://www.cirrus.com>