

## **Errata: CS42416 Rev. C1**

(Reference CS42416\_DS602F1 Data Sheet)

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All references to the CS42416 mentioned below shall be taken to refer to revision C1 of the CS42416 product. The hardware revision code can be found in the 12-character field printed on the last line below the part number of each chip. The seventh and eighth characters from the right indicate the revision code (for instance, YFTAC1AL0311 indicates a Revision C1 part).

- ◆ Upon disabling the PDN bit, the ADC will start-up in one of two modes that affect dynamic range. In one mode, the ADC's dynamic range will meet data sheet specification; in the other, the ADC's dynamic range will exhibit a degradation by approximately 4 dB. Which mode the ADC will enter upon the release of PDN is indeterminate.

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### **Contacting Cirrus Logic Support**

For all product questions and inquiries, contact a Cirrus Logic Sales Representative.

To find the one nearest you, go to <http://www.cirrus.com>